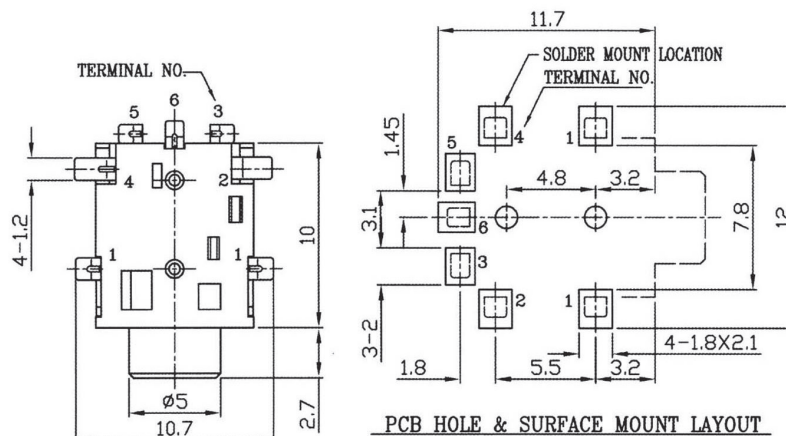


ALTERNATIVE MODELS CIRCUITRY



PCB HOLE & SURFACE MOUNT LAYOUT

SPECIFICATION

1. WITHSTANDING VOLTAGE : 500V AC FOR 1MINUTE
2. INSULATION RESISTANCE: 100MΩ MIN. AT 500V DC
3. CONTACT RESISTANCE: 30mΩ MAX

7	JACK CASING	1	PPA/GF RESIN	BLACK
6	SLEEVE SPRING #6	1	PBs	0.25t Ag-PLATED
5	FIXED TERMINAL #5	1	BRASS	0.25t Ag-PLATED
4	RING SPRING #4	1	PBs	0.25t Ag-PLATED
3	FIXED TERMINAL #3	1	BRASS	0.25t Ag-PLATED
2	TIP SPRING #2	1	PBs	0.3t Ag-PLATED
1	EARTH TERMINAL	1	PBs	0.25t Ag-PLATED
NO. OFF	PART NAME	QTY	MATL	RMK

TOLERANCES X ≤ 5 = ± 0.10 X > 5 = ± 0.30 ANGULAR = ± --- DO NOT SCALE DWG. FINISHED ---	PART NAME		DWG NO. VER. 1	
	ø2.5 STEREO EARPHONE JACK		SCALE	
	MATL		3/1 mm	
	AS SHOWN IN TABLE		DRN	
DATE				



Made in Japan

ELCART DISTRIBUTION SPA via Michelangelo Buonarroti, 46 - 20093 Cologno Monzese (Milano) ITALY
Tel. +39 02.25117310 Fax +39 02.25117610 sito internet: www.elcart.com e-mail: info@elcart.it

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